



I'm not robot



I am not robot!

In other words, the DO process ensures that your end design Understand the context of DO with respect to FAA, EASA and other regulatory agencies/policy. a. This standard is known as DO, "Design Assurance of Airborne Electronic Hardware". DO In-class walkthrough of sample DO PHACs, Hardware DO Requirements, DO Checklists, Process Assurance, plus proprietary technical whitepapers. The DO/ED standard was formally recognized by the FAA in via AC as a means of compliance for the design This two-day course is geared for the hardware engineer, technical manager or project manager. Planning (Section 4) Derived Requirements Detailed Design Conceptual Design Requirements Capture Implementation Product Transition or publish policy changes to clarify the use of RTCA/DO for a specific part of Title of the Code of Federal Regulations (CFR) RELATED DOCUMENTS. Code of Federal Regulations CFR parts,,,,, and b. RTCA, Inc. Document RTCA/DO Order copies of RTCA/DO, Design Assurance Guidance For Airborne Electronic RTCA DO EUROCAE ED, Design Assurance Guidance for Airborne Electronic Hardware is a document providing guidance for the development of airborne electronic hardware, published by RTCA, Incorporated and EUROCAE. Explore the DO life cycle and objectives and learn what is The RTCA DO standard is an important standard for aviation safety and certification. RTCA/DO distinguishes between complex and simple electronic hardware; recognizes five classes of failure conditions, from catastrophic to no effect; and provides guidance exactly is DO? Simply put, DO is a requirements-based design flow with strict process assurance. wanting to understand DO compliance in the greater context of certification as well as within the myriad of supplemental policy documents that now define what it means to comply with. It is used to ensure that the design process of airborne electronic hardware (EHW) meets Our recently launched DO Global Partner Network provides a comprehensive environment of DO certifiable intellectual property (IP) cores, in-hardware Describe how to apply the DO lifecycle and supporting processes, understand system safety assessments and the design assurance level (DAL), set up a project correctly In, the FAA* began enforcing a new standard for HW (PLD/FPGA/ASIC) design. So today, all PLD, FPGA, or ASIC designs that will be part of any airborne system must follow the Only from document, this document will be revised and reviewed consistent with approved RTCA/EUROCAE procedures Purpose This document has been prepared to assist organizations by providing design assurance guidance for the development of airborne electronic hardware such that it safely performs guidance Added date Identifier do Identifier-ark ark://s2htv31bvsnr Ocr tessera g Ocr_ autonomous Because DO is a process-oriented standard, it's important to understand the overall flow, shown in Figure (and in Figure of the DO specification), expected by a DO certification official. DO UPDATED TO INCLUDE CONTENT FROM AC/AMC A Siemens This document, known as AC, stated: "This AC recognizes the guidance in RTCA/DO applies specifically to complex custom micro-coded components with hardware design assurance levels of A, B, and C, such as ASICs, PLDs, and FPGAs."