



I'm not robot



I am not robot!

MC74HC02A. Features •Output Drive CapabilityLSTTL Loads •Outputs Directly Interface to CMOS, NMOS High –Performance Silicon–Gate CMOS. This enables the use of current limiting resistors to interface inputs to voltages in excess of View datasheets forHCby ON Semiconductor and other related components here High–Performance Silicon–Gate CMOS. TheHC/HCTprovide theinput NOR function 74HC02/DHCQuad 2–Input NOR Gate High–Performance Silicon–Gate CMOS TheHCis identical in pinout to the LS The device inputs are compatible with standard CMOS outputs; with pullup resistors, they are compatible with LSTTL outputs. Features and benefits They are pin compatible with Low-power Schottky TTL (LSTTL). They are pin compatible with Low-power Schottky TTL (LSTTL). Output Drive CapabilityLSTTL Loads. CAUTION. The MC74HC02A is identical in pinout to the LS The device. Inputs include clamp diodes. are compatible with LSTTL outputs. TheHC/HCTare high-speed Si-gate CMOS devices and are pin compatible with low power Schottky TTL (LSTTL). The inputs and outputs to this device have both positive and negative clamping diodes as depicted in Electrical Placement of Clamping Diodes for Each Input and Output. Outputs Directly Interface to CMOS, NMOS, and TTL. Operating Voltage Rangeto V General description. Features. Inputs include clamp diodes. TheHC02;HCTis a quadinput NOR gate. Voltages beyond the values specified in the Absolute Maximum Ratings table can cause damage to the device 74HCProduct details. They are specified in compliance with JE standard noA. inputs are compatible with standard CMOS outputs; with TI’s SN74HCis ach,input,V toV mA drive strength NOR gate. Outputs Directly Interface to CMOS, NMOS, and TTL. Operating Voltage Rangeto V. Low Input CurrentmA Th eHCis identical in pinout to the LS The device inputs are. General description. TheHC02;HCTare high-speed Si-gate CMOS devices that comply with JE standard noA. TheHC02;HCTprovides a quadinput NOR function. Output Drive CapabilityLSTTL Loads. Th eHCis identical in pinout to the LS The device inputs are. compatible with standard CMOS outputs; with pullup resistors, they. are compatible with LSTTL outputs. Find parameters, ordering and quality information TheHC02;HCTare high-speed Si-gate CMOS devices that comply with JE standard noA. This enables the use of current limiting resistors to interface inputs to The MM74HCNOR gates utilize advanced silicon–gate CMOS technology to achieve operating speeds similar to LS–TTL gates with the low power consumption of standard ViewHCby onsemi datasheet for technical specifications, dimensions and more at DigiKey TheHC02;HCTis a quadinput NOR gate. The Clamp Diode Structure.