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The 93xx86a devices provide dedicated 8- bit memory organization, while the 93xx86b devices provide. 0v microwire serial eeprom: file size 84. word- selectable devices such as the 93xx86c are dependent upon external logic levels driving the org pin to set word size. 0v microwire serial 93c86 pdf eeprom, 93c86 datasheet, 93c86 circuit, 93c86 data sheet : microchip, alldatasheet, datasheet, datasheet search site for electronic components and semiconductors, integrated circuits, diodes, 93c86 pdf triacs and other semiconductors. download 93c86 datasheet. datasheet: 84kb/ 12p. the cav93c86 is manufactured using on semiconductor' s advanced cmos eeprom floating gate. 16 bits (org pin at vcc) or 8 bits (org pin at gnd). 93c86- p product details.

for dedicated 8- bit communication, the 93xx86a devices are available, while the 93xx86b devices provide. download 93c86 pdf datasheet file. 93c76/ 86 are 8k and 16k low voltage serial electrically erasable proms. the cat93c86 is a 16– kb serial eeprom memory device which is configured as either registers of 16 bits (org pin at vcc) or 8 bits (org pin at gnd).

each register can be written (or read) serially by using the di (or do) pin. high speed operation: – 93c46/ 56/ 57/ 66: 1mhz. the device features sequential read and self– timed internal write with auto– clear. manufacturer: microchip technology. for dedicated 8- bit communication, the 93xx86a devices are available, while the. 93c86 product details. 0v microwire serial eeprom. the cat93c46/ 56/ 57/ 66/ 86 are 1k/ 2k/ 2k/ 4k/ 16k- bit serial e2prom memory devices which are configured as either registers of 16 bits (org pin at vcc) or 8 bits (org pin at gnd). the device memory is configured as x8 or x16 bits depending on the org pin setup. only one device is selected at a time, so only one device drives the serial data output (q) line at a time, the other devices are high.

clk cycles read 1 10 a9 a8 a7 a6 a5 a4 a3 a2 a1 a0 — d15 - d0 29 ewenx x x x x x x — high- z 13. description: 8k/ 16k 5. the microchip technology inc. 93c86a product details.

the microchip technology inc. the m93cx6- w devices operate within a voltage supply. figure 3 shows an example of three memory devices connected to an mcu, on a serial bus. the m93c46 (1 kbit), m93c56 (2 kbit), m93c66 (4 kbit), m93c76 (8 kbit) and m93c86 (16 kbit) are electrically erasable programmable memory (eeprom) devices accessed through the microwire™ bus protocol.

at93c86 product details. 93c86 datasheet (pdf) - microchip technology: part # 93c86: description 8k/ 16k 5. 69 kbytes : html viewnext clk cycles read 1 10 x a8 a7 a6 a5 a4 a3 a2 a1 a0 — d15 - d0 29 ewenx x x x x x x — high- z 13. the cat93c86 is manufactured using onsemi' s advanced cmos eeprom floating gate technology. the cat93c66 is a 4– kb cmos serial eeprom device which is organized as either 256 registers of 16 bits (org pin at vcc) or 512 registers of 8 bits (org pin at gnd).

the memory array can be configured either in bytes (x8b) or in words (x16b). 93xx86a/ b/ c devices are 16- kbit low- voltage serial electrically erasable proms (eeprom). advanced cmos technology makes these devices ideal for low power non- volatile memory applications. table 1- 5: instruction set for 93c86: org= 1 (x16 organization) table 1- 6: instruction set for 93c86: org= 0 (x8 organization) instruction sb opcode address data in data out req. the cav93c86 is an eeprom serial 16– kb microwire automotive pdf grade 1 device, which is configured as either registers of.

2 connecting to the serial bus. 93xx86a/ b/ c devices are 16k bit low- voltage serial electrically erasable proms (eeprom). table 1- 6: instruction set for 93c86: org= 1 (x16 organization) table 1- 7: instruction set for 93c86: org= 0 (x8 organization) instruction sb opcode address data in data out req.