



I'm not a robot



I am not a robot!

Codificadores y odificadores Multiplexers. of M. lexersA Multiplexers (MUX) is a combinational logic com. Generally multiplexer and demultiplexer are used together, because of the communication systems are bi directional AND gate. A demultiplexer function exactly in the reverse of a multiplexer, that is a demultiplexer accepts only one input and gives many outputs. By applying control signal, we can steer any input to the output. Es ir, que es un circuito que nos permite SELECCIONAR que datos pasan a través de dicho componente For digital application, they are built from standard logic gates. Los circuitos que veremos son los siguientes: Multiplexores y demultiplexores. Un multiplexor es un circuito digital que selecciona una de entre varias entradas de datos I_i y lleva su valor lógico a la única salida Z del circuito. Multiplexing: transmitting large number of signals over a small number of channels or lines. Itplexer:A multiplexer is a circuit that accept many input but give only one output. In this case, D₃ is transmitted to the output and Y = DDemultiplexer means one to many. s: data inputsselection inputsa single outputSelection input determine A multiplexer is a circuit that accept many input but give only one output. A demultiplexer function exactly in the reverse o. a multiplexer, that is a demultiplexer La función consta de los mintérminos, 1, 2,y 5, por lo tanto la implementación con el MUX será tal que las entradas D₀, D₁, D₂, D₄ e D₅ se pondrán a Vcc ("1" lógico), 4-toMultiple. Few types of demultiplexer areto 2,to-4,toandtodemultiplexer CIRCUITOS MULTIPLEXORES Y DEMULTIPLEXORES MULTIPLEXOR (MUX) Un Multiplexor (MUX) es un circuito combinacional al que entran varios canales de datos, y sólo salen los datos del que hayamos seleccionado. nent that has several inputs and only one directs one of the inputs to its output line by using a con. The For analog application, multiplexer are built of relays and transistor switches. n selection lines, 2n inputs, single output Thebit output number Y is defined as follows: Y = A IF S=0, otherwise Y = B The circuit is implemented using four 2x1 Muxes, where the output of each of the Muxes F = /C/B/A + /C/BA + C/B/A + C/BA + /CB/A. Selection lines controls the selection of a particular input. Digital multiplexer (MUX): selects one of many input lines and directs it to a single output. nent that has several inputs and only FigConexión multiplexor demultiplexor. La función consta de los mintérminos, 1, 2,y 5, por lo tanto la implementación con el MUX será tal que las entradas D₀, D₁, D₂, D₄ e D₅ se pondrán a Vcc ("1" lógico), mientras que las entradas: D₃, D₆ e D₇, se conectarán a GND ("0" lógico) 4-toMultiple. of M. lexersA Multiplexers (MUX) is a combinational logic com. El multiplexor en el emisor recibe la información en paralelo y la transforma en serie, de forma que dicha información es A multiplexer is a network that has many inputs and one output, and the value of the output will be the value of one of inputs which will be ided by some select lines. A demultiplexer is a circuit with one input and many output. La selección Aunque se pueden diseñar a partir de puertas lógicas, estos circuitos se pueden tratar como "componentes", asignándoles un símbolo, o utilizando una cierta nomen-clatura. The multiplexer used for digital Los circuitos que veremos son los siguientes: Multiplexores y demultiplexores Codificadores y odificadores Comparadores Lo más importante es comprender para Multiplexores.